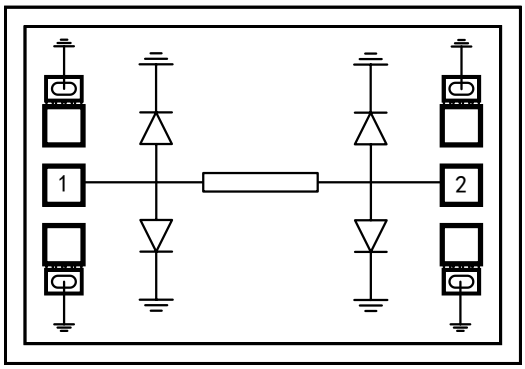
1. Description

- The MA005A is a limiter fabricated using GaAs PIN MMIC process. The device operates from DC to 20GHz, with an insertion loss of less than 0.9dB, input and output return losses of less than -15dB, a power handling capability of 40dBm (CW), and a flat leakage of 15.5dBm. No input or output DC-blocking capacitors are integrated on-chip.



MA005A-PDS-F01

Figure 1. MA005A Functional Block Diagram

2. Applications

- TX/RX Modules

3. Features

- Operating Frequency: DC-20GHz
- Insertion Loss: -0.9dB
- Input/Output Return Loss: -16dB / -15dB
- Power Handling Capability: 40dBm (CW)
- Flat Leakage: 15.5dBm
- Die Size: 1.40 x 0.58 x 0.10 mm

4. Brief Typical Performance

Table 1. Brief Typical Performance

Parameter	DC-20GHz	Units
Insertion Loss	-0.9	dB
Input Return Loss	-16	dB
Output Return Loss	-15	dB
Power Handing Capability	40	dBm
Flat Leakage at P_{IN}>20dBm	15.5	dBm

Test condition: CW Test

Table of Contents

No.	Contents	Page
1	Description	1
2	Applications	1
3	Features	1
4	Brief Typical Performance	1
5	Recommended Operating Conditions	3
6	RF Characteristics	3
7	Typical Performance Curve	3
8	Outline Dimensions	4
9	Assembly Information	5
10	Handling Precautions	5
11	Contact Information	6
12	Disclaimer	6

5. Recommended Operating Conditions

Table 2. Recommended Operating Conditions

Parameter	Min.	Typ.	Max.	Units
Operating Temperature Range	-55	25	125	°C
Operating Frequency	DC	-	20	GHz
Maximum Input Power	-	-	40	dBm

6. RF Characteristics (T_C = 25˚C)

Table 3. RF Characteristics[1]

Parameter	Symbol	Min.	Typ.	Max.	Units
Operating Frequency	Freq	DC	-	20	GHz
Insertion Loss @ 20GHz	IL	-	-0.9	-	dB
Input Return Loss	RLin	-	-16	-	dB
Output Return Loss	RLout	-	-15	-	dB
Power Handing Capability	Pc	-	40	-	dBm
Flat Leakage	FL	-	15.5	-	dBm

[1].Test condition: CW Test

7. Typical Performance Curve

■ Small-signal Characteristics Curve

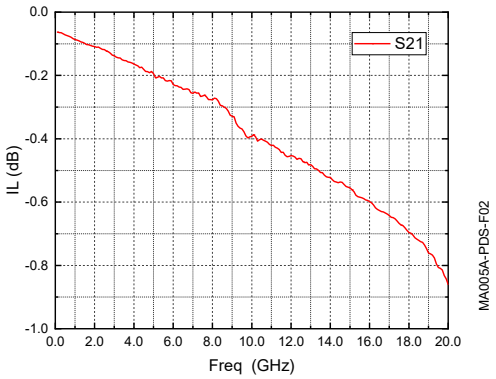


Figure 2. Insertion Loss Characteristics

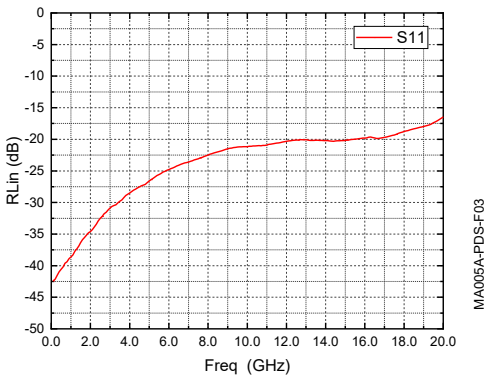


Figure 3. Input Return Loss Characteristics

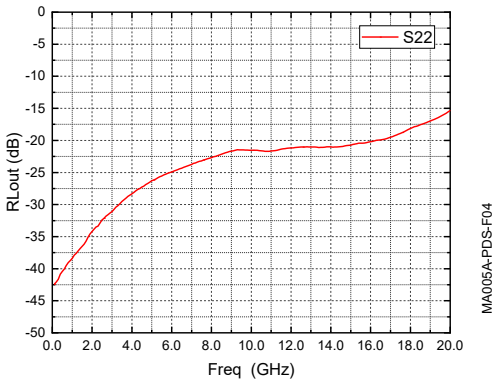


Figure 4. Output Return Loss Characteristics

■ Power Characteristics Curve

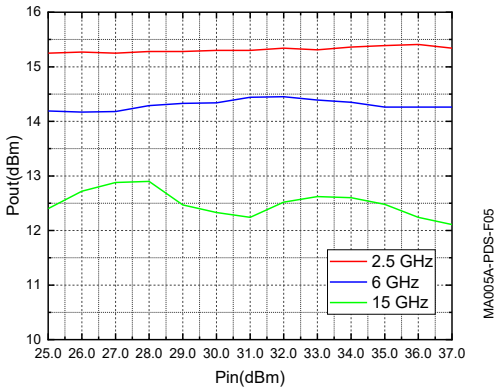
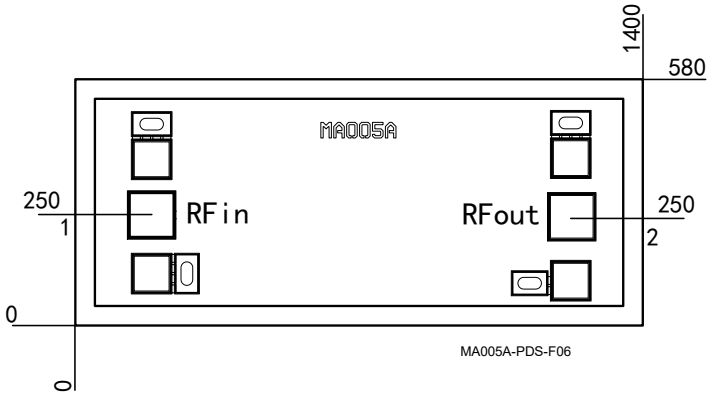


Figure 5. Flat Leakage Characteristics

8. Outline Dimensions



- Dimensions shown in micrometre

Figure 6. MA005A Outline Dimensions

Table 4. Pad Description

Pad	Symbol	Description
1	RFin	RF Input
2	RFout	RF Output

9. Assembly Information

■ **Assembly Diagram**

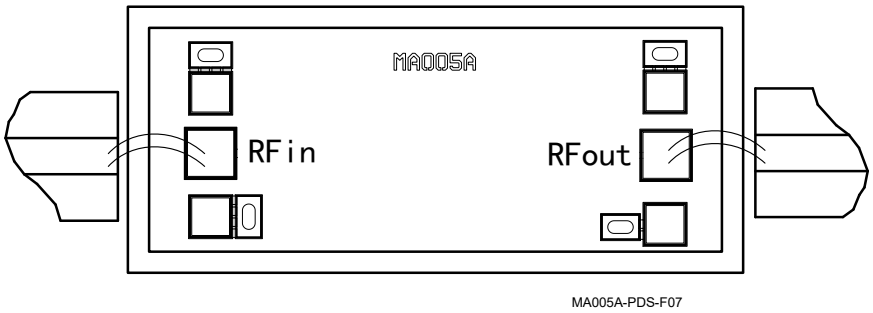


Figure 7. MA005A Assembly Diagram

- **Eutectic Die Attach:** It is best to use an 80% Au/20% Sn preform. Do not expose the chip to a temperature greater than 300°C for more than 30sec.
- **Wire Bonding:** RF bonds made with 1mil gold wire are recommended for the RF ports. Keep all bonds as short as possible, less than 0.35mm.

10. Handling Precautions

■ **Storage**

All bare dies ship in either Waffle or Gel Pak, which is an ESD protective container, and then sealed in an ESD protective bag. After opening the sealed ESD protective bag, all dies must be stored in a dry nitrogen environment.

■ **Cleanliness**

Handle the dies in a clean environment. Never use liquid cleaning systems to clean the dies.

■ **ESD Caution**

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



11. Contact Information

For the latest specifications, additional product information, technical support and sales support, please contact:

■ **Email:** RF.support@raytrontek.com

■ **Web:** www.raytrontek.com

12. Disclaimer

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