1. Description

- The M5312 is a 49dBm ($IP_{-0.3dB}$) wideband SPDT switch fabricated on GaN HEMT MMIC process. It operates over 0.35 to 2GHz with -40V control voltage.

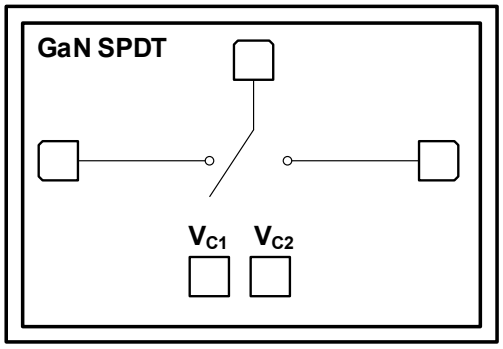


Figure 1. M5312 Functional Block Diagram

2. Applications

- TX/RX Modules

3. Features[1]

- Frequency Range: 0.35-2GHz
- Insertion Loss: 0.9dB
- Isolation: 45dB
- Input/Output Return Loss: -10dB
- Input Power ($IP_{-0.3dB}$): 49dBm
- Control Voltage: 0/-40V
- Die Size: 2.25 x 1.25 x 0.05mm

[1] On-wafer Test

4. Brief Typical Performance

Table 1. Brief Typical Performance

Parameter	0.35-2GHz	Units
Insertion Loss	0.9	dB
Isolation	45	dB
Input / Output Return Loss	-10	dB
Input Power for 0.3dB Compression	49	dBm

Test condition: Input signal Pulsed, Pulse width = 1ms, Duty cycle =20%

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5. Recommended Operating Conditions

Table 2. Recommended Operating Conditions

Parameter	Min.	Typ.	Max.	Units
Operating Temperature Range	-55	25	125	°C
Control Voltage，VC1/VC2	-	0/-40V	-	V

6. RF Characteristics (T_C = 25˚C)

Table 3. RF Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Insertion Loss	IL	-	0.9	-	dB
Isolation	ISO	-	45	-	dB
Input/Output Return Loss	RL_{IN}/ RL_{OUT}	-	-10	-	dB
Input Power for 0.3dB Compression	$IP_{-0.3dB}$	-	49	-	dBm

Test condition: Input signal Pulsed, Pulse width = 1ms, Duty cycle =20%

7. Typical Performance Curve

■ Small-signal Characteristics Curve（25℃）

- **V_{CTL}=-40V/0V, On-Wafer Test**

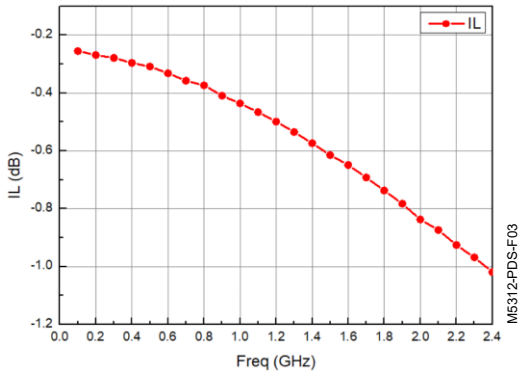


Figure 2. Insertion Loss Characteristics

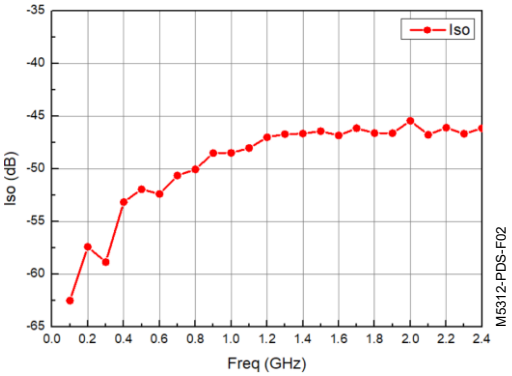


Figure 3. Isolation Characteristics

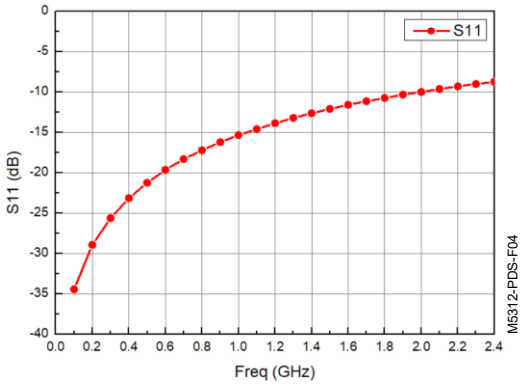


Figure 4. Input Return Loss Characteristics

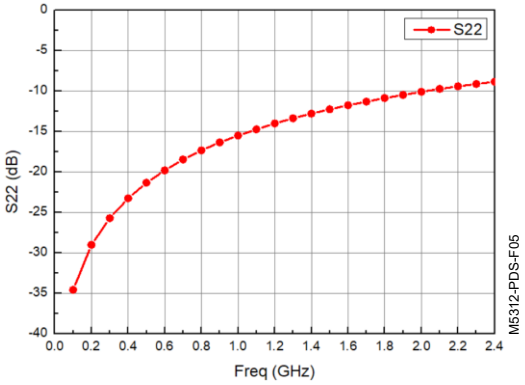


Figure 5. Output Return Loss Characteristics

■ Power Characteristic Curve（25℃）

- **V_{CTL}=-40V/0V, Fixture test**

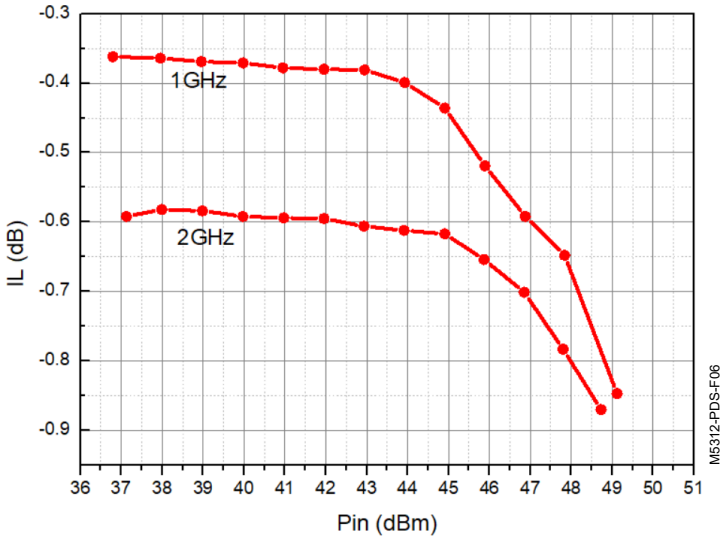
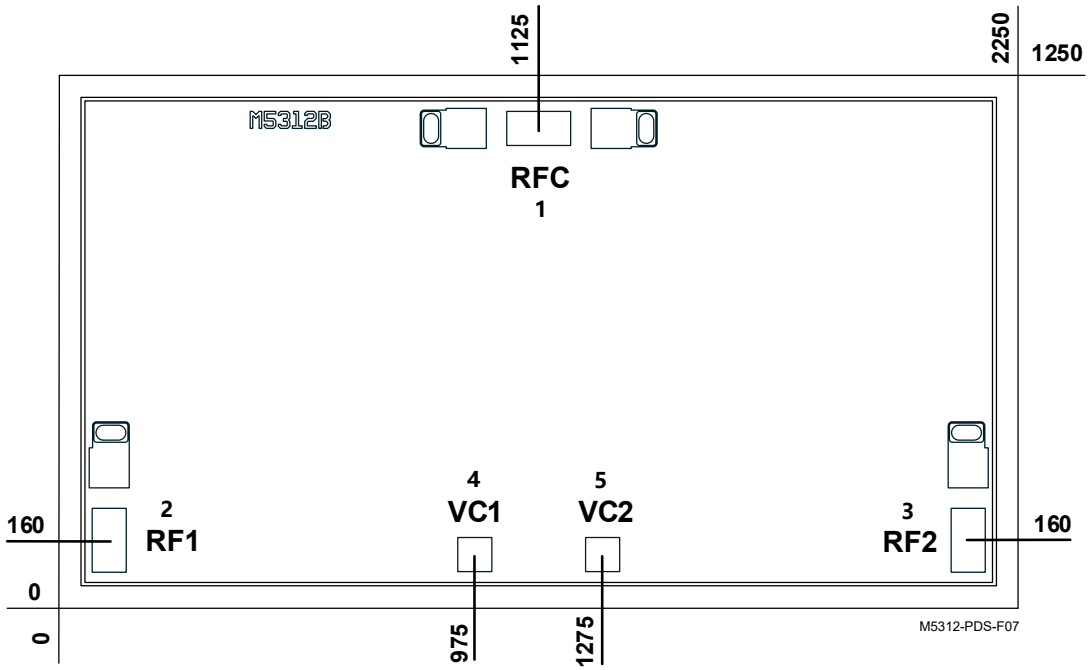


Figure 6. IL vs. P_{in} Characteristics

8. Outline Dimensions



- Dimensions shown in micrometre

Figure 7. M5312 Outline Dimensions

Table 4. Pad Description

Pad	Symbol	Description
1	RFC	RF Input
2,3	RF1/RF2	RF Output 1 / RF Output 2
4,5	V_{C1}/V_{C2}	Control Voltage

Table 5. Truth Table

V_{C1}	V_{C2}	RFC to RF1	RFC to RF2
0V	-40V	ON	OFF
-40V	0V	OFF	ON

9. Assembly Information

■ Assembly Diagram

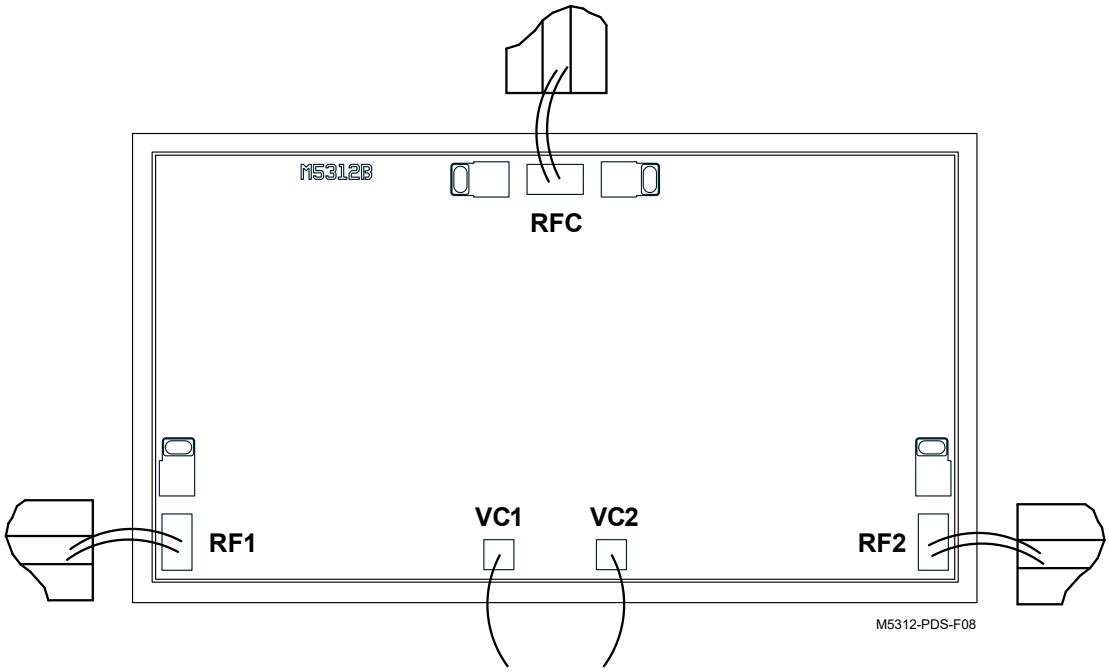


Figure 8. M5312 Assembly Diagram

- **Eutectic Die Attach:** It is best to use an 80% Au/20% Sn preform. Do not expose the chip to a temperature greater than 300°C for more than 30sec.
- **Wire Bonding:** RF bonds made with 1mil gold wire are recommended for the RF ports. Keep all bonds as short as possible, less than 0.35mm.

10. Handling Precautions

■ Storage

All bare dies ship in either Waffle or Gel Pak, which is an ESD protective container, and then sealed in an ESD protective bag. After opening the sealed ESD protective bag, all dies must be stored in a dry nitrogen environment.

■ Cleanliness

Handle the dies in a clean environment. Never use liquid cleaning systems to clean the dies.

■ ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



11. Contact Information

For the latest specifications, additional product information, technical support and sales support, please contact:

- **Email:** RF.support@raytrontek.com
- **Web:** www.raytrontek.com

12. Disclaimer

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