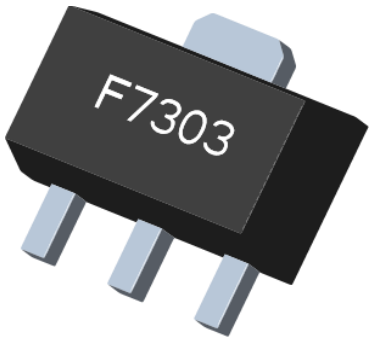
1. Description

- The F7303 is a wideband GaAs low phase noise amplifier for wireless application. It is internally integrated with an active bias circuity and can provide stable current over temperature and process variation.
- The F7303 is a high linearity gain block for application that require small in size with minimal number of external components. It is internally matched to 50Ω at input and output.



F7303-PDS-F01

Figure 1. F7303 Package Outline

2. Applications

- Data link Communication
- Radar
- Small Cell
- Private Communication Network

3. Features[1]

- Operating Frequency: 10-6000MHz
- Output Power (P1dB): 19dBm
- Small Signal Gain: 19.2dB
- Phase Noise:-172dBc/Hz@10KHz Offset
- Operating Voltage: 5V
- Package: SOT-89

[1]. Measured in Raytron DEMO @1950MHz

4. Brief Typical Performance

Table 1. Brief Typical Performance

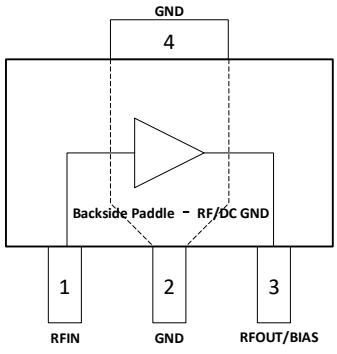
Parameter[1]	1950MHz	Units
Small Signal Gain	19.5	dB
Output P1dB[2]	18.8	dBm
Output IP3[3]	31	dBm

[1]. V_{CC}=5V, I_{CQ}=70mA,Temp=25℃, 50Ω test system

[2]. Input signal: CW

[3]. Input signal: 2-tone, P_{OUT}=0dBm/tone, Δf=1MHz

5. Block Diagram



F7303-PDS-F02

Figure 2. F7303 Block Diagram TOP View

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6. Absolute Maximum Ratings

Table 2. Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Operating Voltage	V_{CC}	5.5	V
Input Power	P_{IN}	24	dBm
Storage Temperature Range	T_{STG}	- 55 ~ +125	°C
Operating Junction Temperature	T_J	150	°C

7. Recommended Operating Conditions

Table 3. Recommended Operating Conditions

Parameter	Min.	Typ.	Max.	Units
Operating Temperature Range	-55	25	105	°C
Operating Voltage	-	5.0	5.5	V
Operating Frequency	30	-	6000	MHz

8. Electrical Characteristics (T_C = 25°C)

Table 4. Electrical Characteristics (V_{CC}=5V, L1=51nH, I_{CQ}=70mA, T_C=25℃, 50Ω test system)

Parameter	Conditions	Min.	Typ.	Max.	Units
Frequency	-	500	-	6000	MHz
Small Signal Gain	850MHz	-	21.0	-	dB
	1950MHz	-	19.5	-	dB
	2600MHz	-	18.6	-	dB
	3500MHz	-	16.9	-	dB
	4900MHz	-	15.0	-	dB
	5800MHz	-	11.6	-	dB
Input Return Loss	2600MHz	-	16.0	-	dB
Output Return Loss	2600MHz	-	8.4	-	dB
Output P1dB	850MHz	-	19.6	-	dBm
	1950MHz	-	18.8	-	dBm
	2600MHz	-	18.5	-	dBm
	3500MHz	-	16.6	-	dBm
	4900MHz	-	13.7	-	dBm
Output IP3 P_{OUT}=0dBm/tone, Δf=1MHz	850MHz	-	33.2	-	dBm
	1950MHz	-	31.0	-	dBm
	2600MHz	-	28.7	-	dBm
	3500MHz	-	25.5	-	dBm
	4900MHz	-	21.2	-	dBm
Device Operating Current	-	-	70	-	mA

Table 5. Electrical Characteristics (V_{CC}=5V, L1=330nH, I_{CQ}=69mA, T_C=25℃, 50Ω test system)

Parameter	Conditions	Min.	Typ.	Max.	Units
Frequency	-	100	-	2000	MHz
Small Signal Gain	100MHz	-	21.5	-	dB
	500MHz	-	21.2	-	dB
	1000MHz	-	20.7	-	dB
	1500MHz	-	20.0	-	dB
	2000MHz	-	19.2	-	dB
Input Return Loss	1000MHz	-	33.8	-	dB
Output Return Loss	1000MHz	-	15.3	-	dB
Output P1dB	100MHz	-	18.5	-	dBm
	500MHz	-	18.9	-	dBm
	1000MHz	-	18.4	-	dBm
	1500MHz	-	17.7	-	dBm
	2000MHz	-	17.3	-	dBm
Output IP3 P_{OUT}=0dBm/tone, Δf=1MHz	100MHz	-	30.6	-	dBm
	500MHz	-	32.0	-	dBm
	1000MHz	-	31.3	-	dBm
	1500MHz	-	30.2	-	dBm
	2000MHz	-	29.3	-	dBm
Device Operating Current	-	-	69	-	mA

Table 6. Electrical Characteristics (V_{CC}=5V, L1=4.7uH, I_{CQ}=62mA, T_C=25℃, 50Ω test system)

Parameter	Conditions	Min.	Typ.	Max.	Units
Frequency	-	20	-	1200	MHz
Small Signal Gain	20MHz	-	22.8	-	dB
	100MHz	-	21.5	-	dB
	500MHz	-	21.1	-	dB
	1200MHz	-	19.5	-	dB
Input Return Loss	500MHz	-	21.5	-	dB
Output Return Loss	500MHz	-	14.3	-	dB
Output P1dB	20MHz	-	17.6	-	dBm
	100MHz	-	18.3	-	dBm
	500MHz	-	18.3	-	dBm
	1200MHz	-	15.4	-	dBm
Output IP3 P_{OUT}=0dBm/tone, Δf=1MHz	20MHz	-	30.5	-	dBm
	100MHz	-	30.6	-	dBm
	500MHz	-	30.8	-	dBm
	1200MHz	-	27.3	-	dBm
Device Operating Current	-	-	62	-	mA

9. Phase Noise

Table 7. Phase Noise

Offset Frequency (Hz)		1K	10K	100K	1M
Phase Noise(dBc/Hz)[1]	RFfreq=100MHz	-164	-166	-166	-167
	RFfreq=500MHz	-160	-167	-169	-169
	RFfreq=1000MHz	-159	-172	-170	-170
	RFfreq=1500MHz	-165	-167	-168	-169
	RFfreq=2000MHz	-158	-165	-168	-168

[1]Test Condition: RF Input Power=IP_{-1dB}, L1=330nH

10. Typical Performance

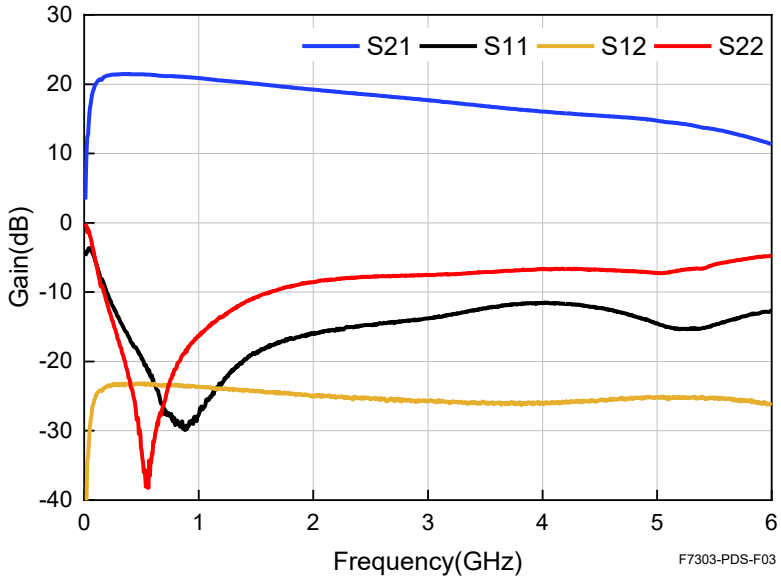


Figure 3. Small Signal S-Parameters vs. Freq.(500-6000MHz)

11. Package Outline

■ Pin Layout

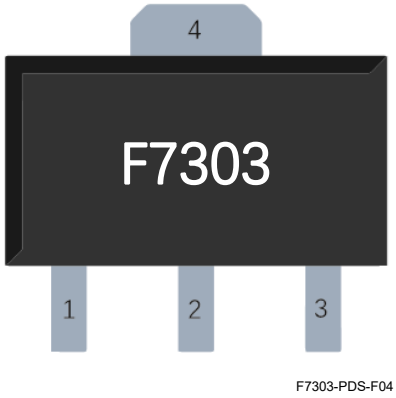


Figure 4. F7303 Pin Layout

Table 8. Pin Description

Pin	Symbol	Description
1	RF_{IN}	RF Input. External DC Block capacitor required.
3	RF_{OUT}/BIAS	RF Output and DC supply input. External DC Block capacitor required.
2/4	GND	RF/DC ground

■ Mechanical Drawing

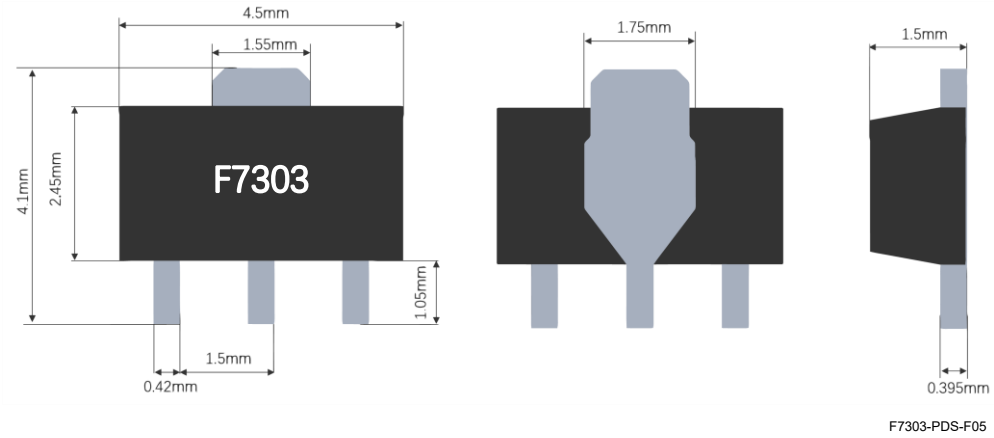


Figure 5. F7303 Mechanical Drawing

12. Application Circuit

■ Schematic

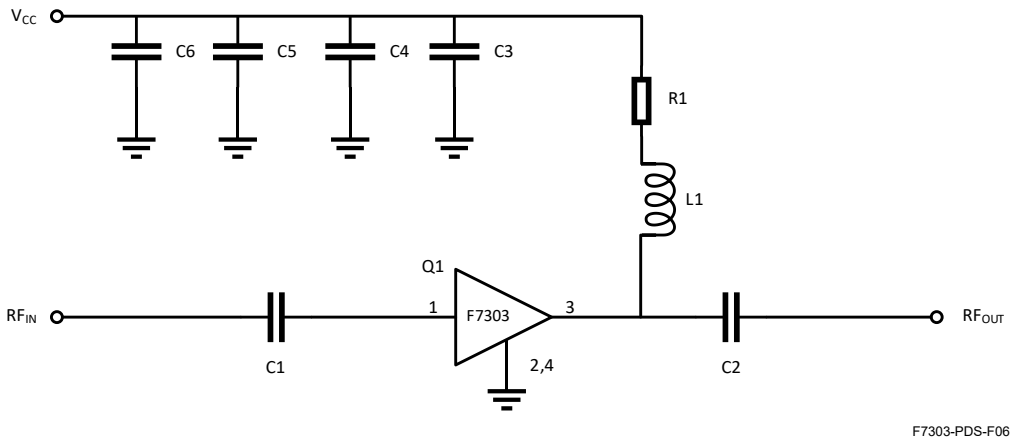
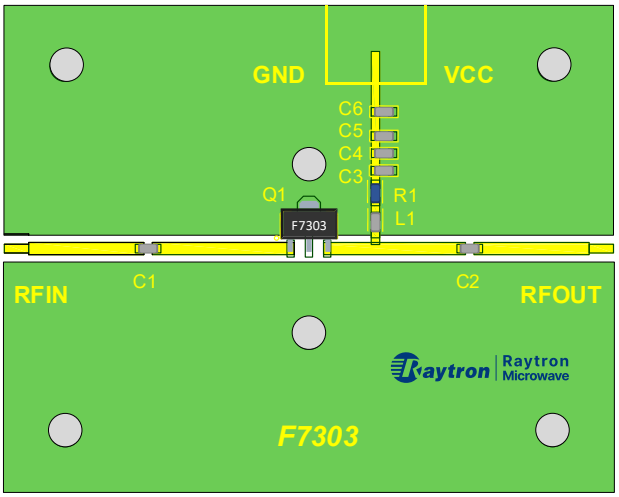


Figure 6. F7303 Application Circuit Schematic

■ Layout



F7303-PDS-F07

Figure 7. F7303 Application Circuit Layout

Table 9. Bill of Materials (20-1200MHz)

Component	Value	Description	Manufacturer	Part Number
PCB	20mil	N/A	Rogers	RO4350B
Q1	N/A	F7303 Amplifier, SOT-89 pkg.	Raytron	F7303
C1	2.2uF	0603 Capacitor	Various	N/A
C2	2.2uF	0603 Capacitor	Various	N/A
C3	10.0pF	C0G 250V 0603 Capacitor	Murata	GQM1875C2E100GB12
C4	1.0nF	0603 Capacitor	Various	N/A
C5	2.2uF	0603 Capacitor	Various	N/A
C6	N/A	N/A	N/A	N/A
L1	4.7uH	1008 Inductance	Murata	LQW2UAS4R7J00L
R1	0Ω	1/4W 0603 Resistor	Various	N/A

Table 10. Bill of Materials (100-2000MHz)

Component	Value	Description	Manufacturer	Part Number
PCB	20mil	N/A	Rogers	RO4350B
Q1	N/A	F7303 Amplifier, SOT-89 pkg.	Raytron	F7303
C1	1.0nF	0603 Capacitor	Various	N/A
C2	1.0nF	0603 Capacitor	Various	N/A
C3	10.0pF	C0G 250V 0603 Capacitor	Murata	GQM1875C2E100GB12
C4	1.0nF	0603 Capacitor	Various	N/A
C5	2.2uF	0603 Capacitor	Various	N/A
C6	N/A	N/A	N/A	N/A
L1	330.0nH	High Q 0603 Inductance	Murata	LQW18ANR33G80D
R1	0Ω	1/4W 0603 Resistor	Various	N/A

Table 11. Bill of Materials (500-6000MHz)

Component	Value	Description	Manufacturer	Part Number
PCB	20mil	N/A	Rogers	RO4350B
Q1	N/A	F7303 Amplifier, SOT-89 pkg.	Raytron	F7303
C1	1.0nF	0603 Capacitor	Various	N/A
C2	1.0nF	0603 Capacitor	Various	N/A
C3	10.0pF	C0G 250V 0603 Capacitor	Murata	GQM1875C2E100GB12
C4	1.0nF	0603 Capacitor	Various	N/A
C5	2.2uF	0603 Capacitor	Various	N/A
C6	N/A	N/A	N/A	N/A
L1	51.0nH	High Q 0603 Inductance	Murata	LQW18AN51NG80D
R1	0Ω	1/4W 0603 Resistor	Various	N/A

13. Handling Precautions

- **ESD Caution**

Table 12. ESD/MSL Rating

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1C	ESDA / JEDEC JS-001-2014
ESD – Charged Device Model (CDM)	Class C3	ESDA / JEDEC JS-002-2014
MSL – Moisture Sensitivity Level	Level 3	IPC/JEDEC J-STD-020

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



14. Contact Information

For the latest specifications, additional product information, technical support and sales support, please contact:

- **Email:** RF.support@raytrontek.com
- **Web:** www.raytrontek.com

15. Disclaimer

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