1. Description

- The D1553 is a 50W (P_{SAT}) wideband matched gallium nitride (GaN) high electron mobility transistor (HEMT) with first generation RF GaN technology from Raytron.
- The D1553 is matched to 50ohm on the input and 50ohm on the output. The device operates from 6.0 to 8.0GHz with a 28V supply rail. It is offered in a small hermetically sealed metal/ceramic flange package.

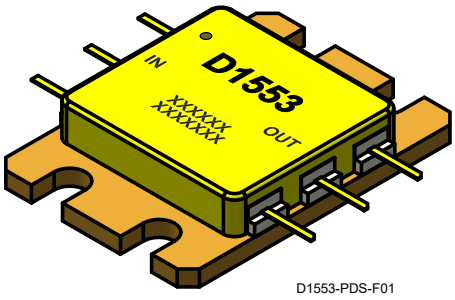


Figure 1. D1553 Package Outline

2. Applications

- Broadband Amplifier
- Test Instrumentation

3. Features[1]

- Operating Frequency: 6.0–8.0GHz
- Output Power (P_{SAT}): 47.7dBm (59W)
- Linear Gain: 11.2dB
- Typical DE @ P_{SAT}: 45.0%
- Operating Voltage: 28V
- CW and Pulse Capable
- Impedance Matched Z_{IN}/Z_{OUT} = 50ohm
- Hermetically Sealed Package
- Package Type: 630F-B

[1] Measured in Raytron DEMO_A

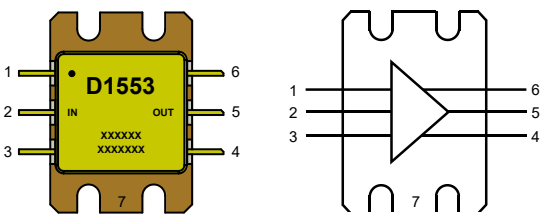
4. Brief Typical Performance

Table 1. Brief Typical Performance

Parameter	6.0GHz	6.5GHz	7.0GHz	7.5GHz	8.0GHz	Units
Linear Gain	11.8	17.4	12.1	12.2	11.2	dB
Drain Efficiency	48.5	73.4	45.6	50.7	45.0	%
Output Power	48.7	48.5	48.2	48.4	47.7	dBm
Output Power	74	71	66	69	59	W

- Typical Class AB performance in Raytron DEMO_A with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 300mA, V_{GS} = -2.97V
- Test Condition: CW

5. Block Diagram



1. **NC**
2. **RF_{IN}**
3. **Gate**
4. **Drain**
5. **RF_{OUT}**
6. **NC**
7. **Source**

553-PDS-F02

Figure 2. D1553 Block Diagram

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6. Absolute Maximum Ratings

Table 2. Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DSS}	84	V
Gate-Source Voltage	V_{GS}	-10 ~ +1	V
Operating Voltage	V_{DS}	10 ~ +32	V
Maximum Forward Gate Current	I_{GMax}	13.44	mA
Storage Temperature Range	T_{STG}	-65 ~ +150	°C
Operating Junction Temperature	T_J	250	°C

7. Recommended Operating Conditions

Table 3. Recommended Operating Conditions

Parameter	Min.	Typ.	Max.	Units
Operating Voltage, V_{DS}	-	28	-	V
Drain Bias Current, I_{DS}	-	300	-	mA
Gate-Source Voltage, V_{GS}	-	-2.97	-	V
Channel Temperature, T_{CH}	-	-	225	°C

Table 4. Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance (Active Die Surface-to-Case)[1]	R_{thsc} (IR)	1.15	°C/W
Thermal Resistance (Junction-to-Case)[2]	R_{thjc} (FEA)	2.19	°C/W

[1]. Thermal Resistance at average power by infrared measurement, Active Die Surface-to-Case, $T_{base\text{-}plate}$ = 85°C, P_D = 46.0W

[2]. Thermal Resistance at average power by Finite Element Analysis, Junction-to-Case, $T_{base\text{-}plate}$ = 85°C, $T_{junction}$ = 200°C

8. Electrical Characteristics (T_C = 25˚C)

Table 5. DC Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage[1] (V_{GS} = -10V, I_{DS} = 13.44mA)	$V_{DS(BR)}$	84	-	-	V
Saturated Drain Current[2] (V_{DS} = 10V, V_{GS} = 1.0V)	I_{Max}	-	15.46	-	A
Gate Threshold Voltage[3] (V_{DS} = 28V, I_{DS} = 13.44mA)	$V_{GS(TH)}$	-3.30	-2.80	-2.30	V
Gate Quiescent Voltage[3] (V_{DS} = 28V, I_{DS} = 300mA)	$V_{GS(Q)}$	-	-2.97	-	V

[1]. Measured on wafer prior to packaging

[2]. Measured with packaged device

[3]. Measured in Raytron DEMO

Table 6. RF Characteristics[1,2]

Freq.	Linear Gain (G_{LIN})	Drain Efficiency (η_D)	Out Power (P_{SAT})
6.0GHz	11.8dB	48.5%	48.7dBm (74W)
7.0GHz	12.1dB	45.6%	48.2dBm (66W)
8.0GHz	11.2dB	45.0%	47.7dBm (59W)

1. Typical Class AB performance in Raytron DEMO_A with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 300mA, V_{GS} = -2.97V
2. Test Condition: CW

9. Typical Performance

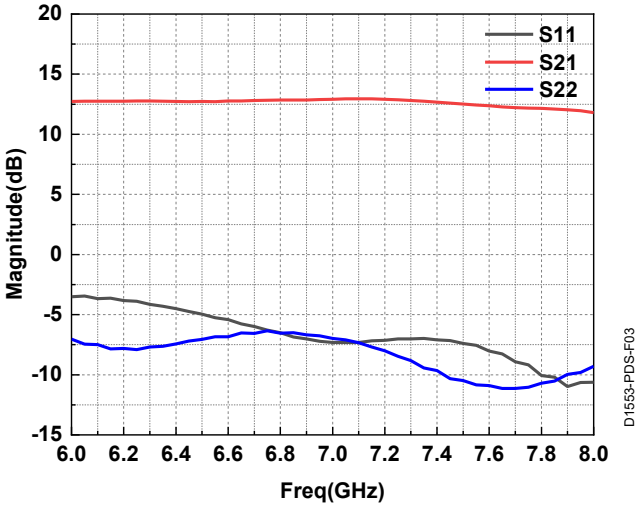


Figure 3. 6.0-7.0GHz S_{11}, S_{21} and S_{22} vs. Freq.

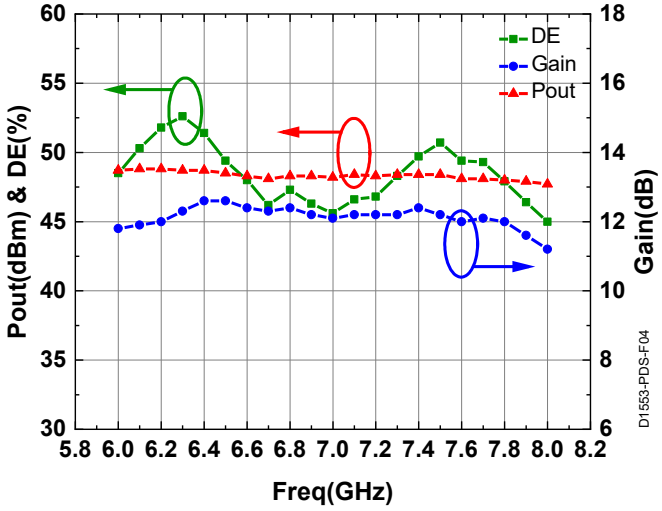


Figure 4. 6.0-8.0GHz Gain and Pout vs. Freq.

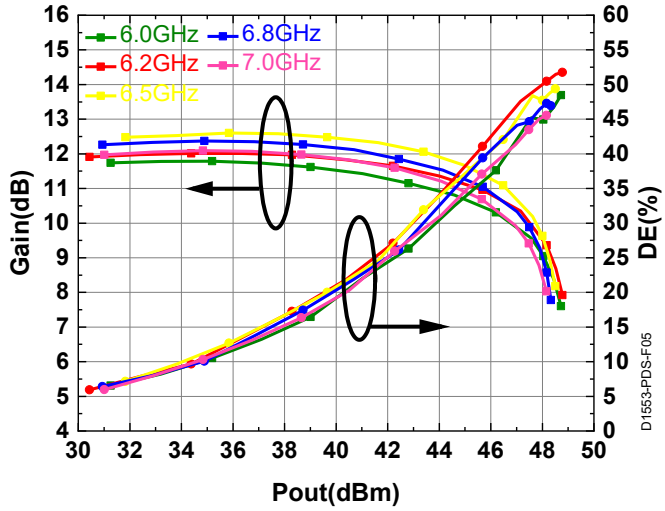


Figure 5. 6.0-8.0GHz Gain and DE vs. P_{OUT}

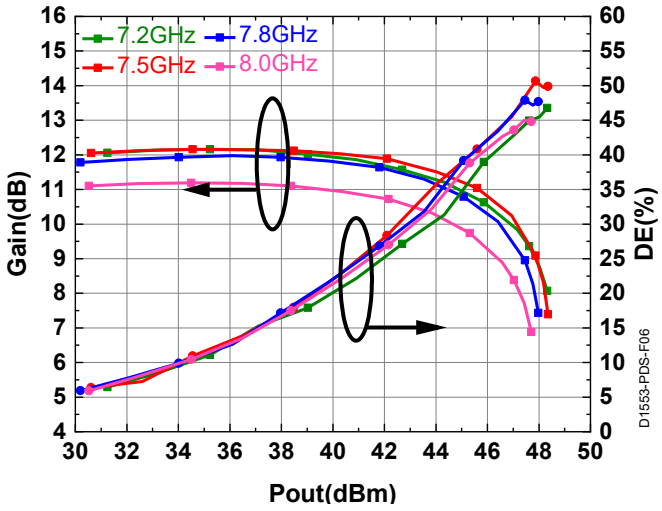


Figure 6. 6.0-8.0GHz Gain and DE vs. P_{OUT}

- Typical Class AB performance in Raytron DEMO_A with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 300mA, V_{GS} = -2.97V
- Test Condition: CW

Table 7. RF Characteristics @ DEMO_A[1,2,3], 6.0-8.0GHz, CW, 28V

Freq.	Linear Gain (G_{LIN})	Drain Efficiency (η_D)	Out Power (P_{SAT})
6.0GHz	11.8dB	48.5%	48.7dBm (74W)
6.1GHz	11.9dB	50.3%	48.8dBm (76W)
6.2GHz	12.0dB	51.8%	48.8dBm (76W)
6.3GHz	12.3dB	52.6%	48.7dBm (74W)
6.4GHz	12.6dB	51.4%	48.7dBm (74W)
6.5GHz	12.6dB	49.4%	48.5dBm (71W)
6.6GHz	12.4dB	48.0%	48.3dBm (68W)
6.7GHz	12.2dB	46.2%	48.1dBm (65W)
6.8GHz	12.1dB	47.3%	48.3dBm (68W)
6.9GHz	12.2dB	46.3%	48.3dBm (68W)
7.0GHz	12.1dB	45.6%	48.2dBm (66W)
7.1GHz	12.2dB	46.6%	48.4dBm (69W)
7.2GHz	12.2dB	46.8%	48.3dBm (68W)
7.3GHz	12.2dB	48.3%	48.4dBm (69W)
7.4GHz	12.4dB	49.7%	48.4dBm (69W)
7.5GHz	12.2dB	50.7%	48.4dBm (69W)
7.6GHz	12.0dB	49.4%	48.1dBm (65W)
7.7GHz	12.1dB	49.3%	48.1dBm (65W)
7.8GHz	12.0dB	47.9%	48.0dBm (63W)
7.9GHz	11.6dB	46.4%	47.9dBm (62W)
8.0GHz	11.2dB	45.0%	47.7dBm (59W)

1. Typical Class AB performance in Raytron DEMO_A with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 300mA, V_{GS} = -2.97V
2. Test Condition: CW
3. The output power reaches 47.7dBm (59W) at 6.0-8.0GHz when V_{DS}=28V.

10. Package Outline

■ Pin Layout

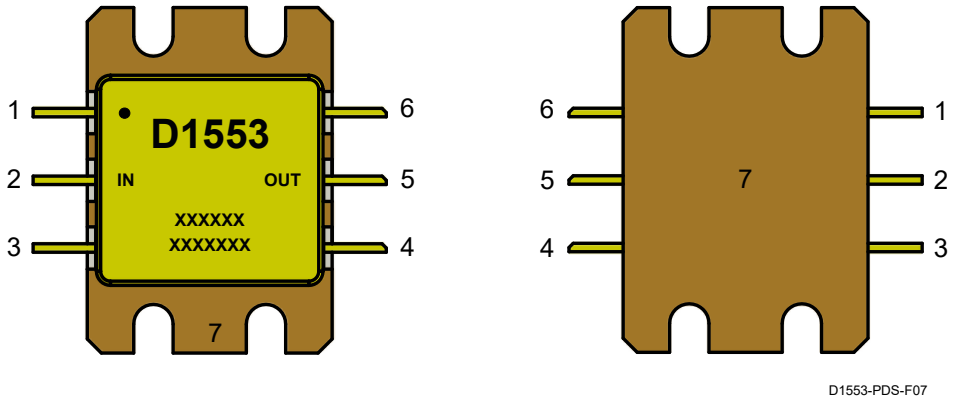


Figure 7. D1553 Pin Layout

Table 8. Pin Description

Pin	Symbol	Description
1	V_G	Gate Voltage
2	RF_{IN}	RF Input
3	V_G	Gate Voltage
4	V_D	Drain Voltage
5	RF_{OUT}	RF Output
6	V_D	Drain Voltage
7	Flange	Source to be Connected to Ground

■ Mechanical Drawing (Package Type:630F-B)

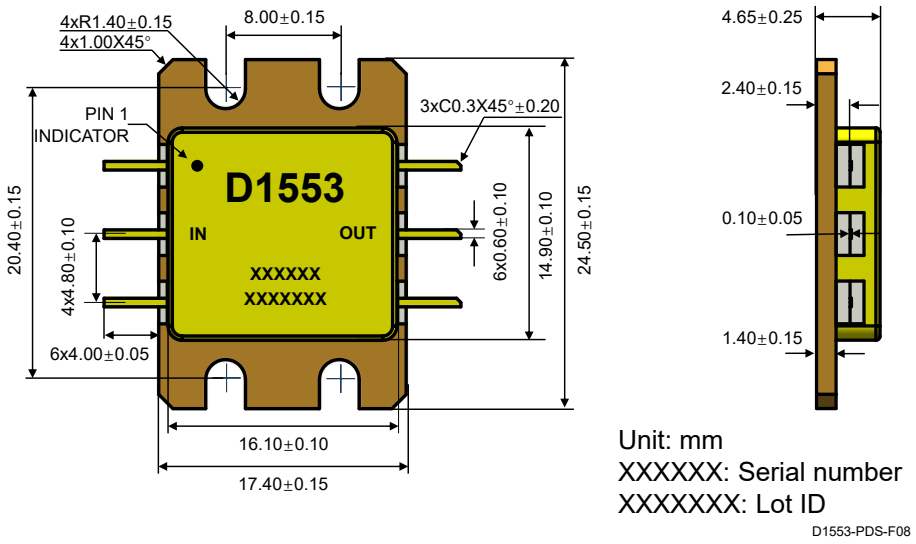


Figure 8. D1553 Mechanical Drawing

11. Application Circuit

■ Schematic

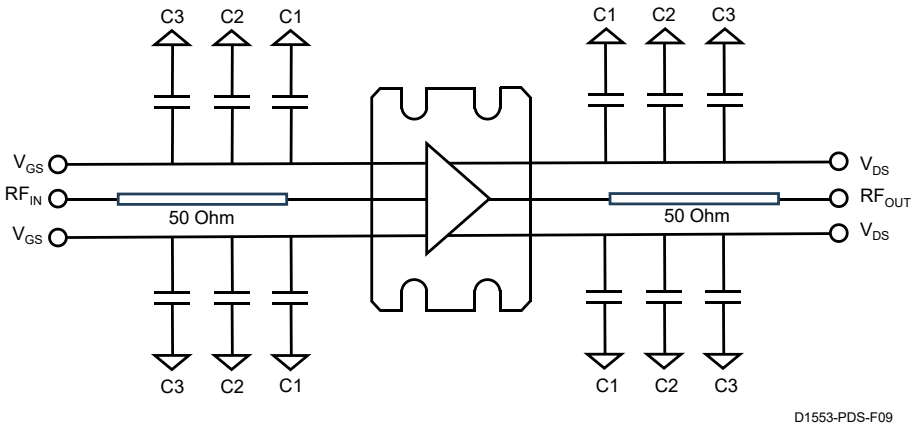


Figure 9. D1553 DEMO_A Application Circuit Schematic

Table 9. Bias Procedure

Bias-up Procedure	Bias-down Procedure
1) Set V_{GS} to the pinch-off voltage, typically -6V	1) Turn off RF signal
2) Turn on V_{DS} to nominal supply voltage (28V)	2) Turn off V_{DS}
3) Slowly adjust V_{GS} until I_{DS} current is attained	3) Wait two seconds to allow drain capacitor to discharge
4) Apply RF input power to desired level	4) Turn off V_{GS}

■ Layout

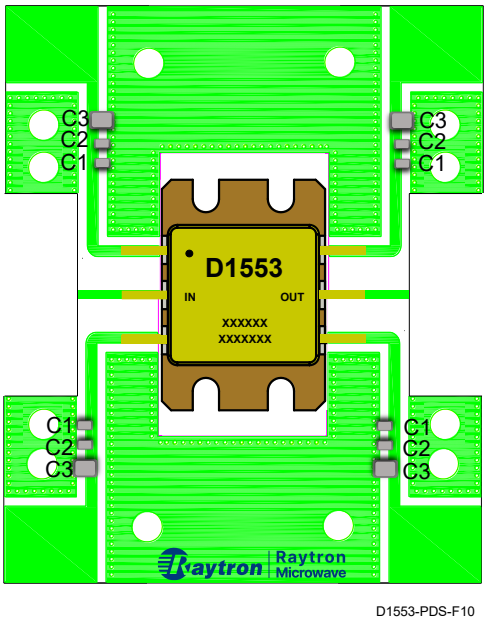


Figure 10. D1553 Application Circuit Layout
（DEMO_A Size: 6.3cm x 5.0cm）

Table 10. Bill of Materials

Component	Value	Description	Manufacturer	Part Number
PCB	20mil	N/A	Rogers	RO4350B
C1	100.0pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2E101JB12
C2	10.0nF	X7R 100V 0603 Capacitor	Murata	GCM188R72A103KA37D
C3	1.0uF	X7R 100V 1210 Capacitor	Murata	GRM3195C2A104JA01

12. DEMO Summary

Table 11. DEMO Summary

NO.	D1553 DEMO Name	V_{DS} (V)	Freq. (GHz)	Linear Gain (dB)	P_{SAT} (dBm)	P_{SAT} (W)	Drain Efficiency (%)	Pulse or CW
1	DEMO_A, 50W	28	6.0-8.0	11.2	47.7	59	45.0	CW

13. Recommended Solder Temperature Profile

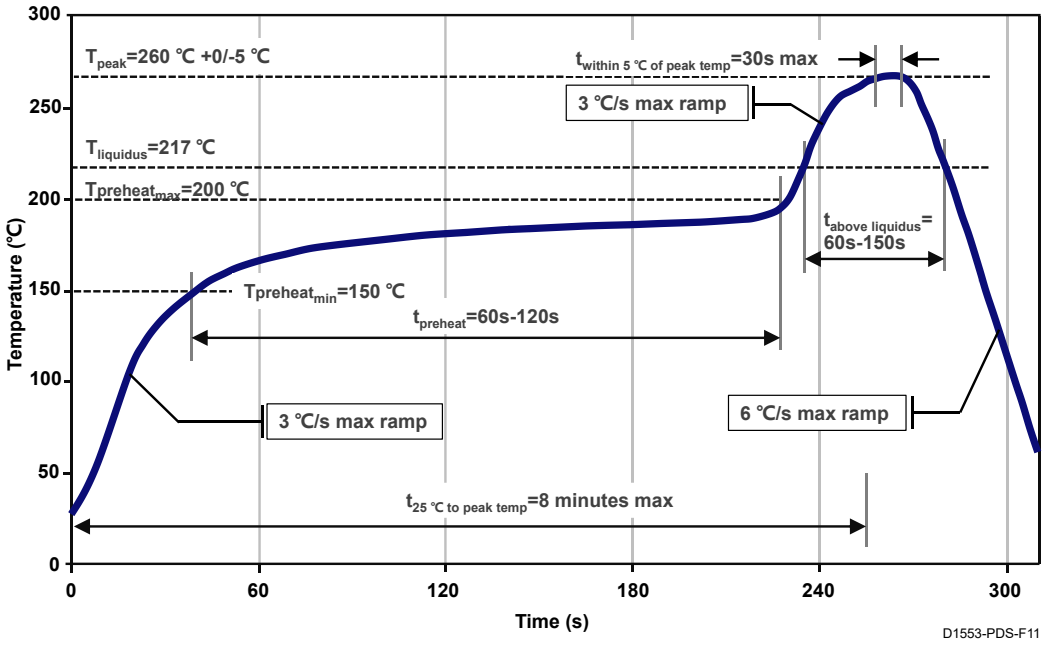


Figure 11. Recommended Solder Temperature Profile

14. Handling Precautions

- **ESD Caution**

  ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



15. Contact Information

For the latest specifications, additional product information, technical support and sales support, please contact:

- **Email:** RF.support@raytrontek.com
- **Web:** www.raytrontek.com

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