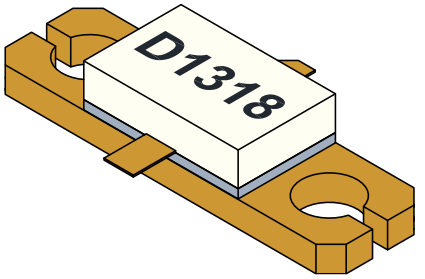
1. Description

- The D1318 is a 126W (P_{SAT}) wideband pre-matched gallium nitride (GaN) high electron mobility transistor (HEMT) with the first-generation RF GaN technology from Raytron.
- The D1318 operates from 2.1 to 3.1GHz with a 28V supply rail. The device is offered in an industry standard air cavity package.



D1318-PDS-F01

Figure 1. D1318 Package Outline

2. Applications

- Military and Civilian Radar
- Land Mobile and Military Radio Communications
- Avionics and Test Instrumentation

3. Features[1]

- Operating Frequency: 2.1-3.1GHz
- Output Power (P_{SAT}): 51dBm (126W)
- Linear Gain: 17.5dB
- Typical DE @ P_{SAT}: 77.2%
- Operating Voltage: 28V
- CW and Pulse Capable
- Package Type: 360F-A

[1] Measured in Raytron DEMO @2.6GHz

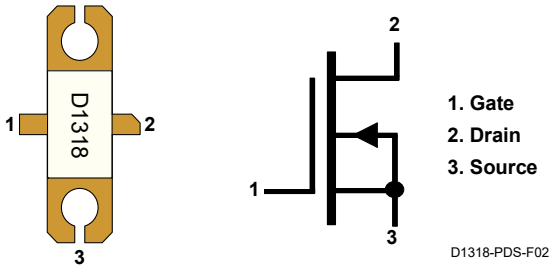
4. Brief Typical Performance

Table 1. Brief Typical Performance

Parameter	2.7GHz	2.8GHz	2.9GHz	3.0GHz	Units
Linear Gain	15.2	15.2	15.3	15.0	dB
Drain Efficiency	60.6	62.6	63.3	63.4	%
Output Power	50.1	49.9	49.6	49.2	dBm
Output Power	102	98	91	83	W

- Typical Class AB performance in Raytron DEMO with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 250mA, V_{GS} = -2.82V
- Test Condition: CW

5. Block Diagram



D1318-PDS-F02

Figure 2. D1318 Block Diagram

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6. Absolute Maximum Ratings

Table 2. Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DSS}	84	V
Gate-Source Voltage	V_{GS}	-10 ~ +1	V
Operating Voltage	V_{DS}	10 ~ +32	V
Maximum Forward Gate Current	I_{GMax}	18.50	mA
Storage Temperature Range	T_{STG}	-65 ~ +150	°C
Operating Junction Temperature	T_J	250	°C

7. Recommended Operating Conditions

Table 3. Recommended Operating Conditions

Parameter	Min.	Typ.	Max.	Units
Drain-Source Voltage Range, V_{DS}	-	28	32	V
Drain Bias Current, I_{DS}	-	250	-	mA
Gate-Source Voltage, V_{GS}	-	-2.95	-	V
Channel Temperature, T_{CH}	-	-	225	°C

Table 4. Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance (Active Die Surface-to-Case)[1]	R_{thsc} (IR)	1.31	°C/W
Thermal Resistance (Junction-to-Case)[2]	R_{thjc} (FEA)	1.61	°C/W

1. Thermal Resistance at average power by infrared measurement, Active Die Surface-to-Case, $T_{base\text{-}plate}$ = 85℃, P_D = 64.0W

2. Thermal Resistance at average power by Finite Element Analysis, Junction-to-Case, $T_{base\text{-}plate}$ = 85℃, $T_{junction}$ = 200℃

8. Electrical Characteristics (T_C = 25˚C)

Table 5. DC Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage[1] (V_{GS} = -10V, I_{DS} = 18.5mA)	$V_{DS(BR)}$	84	-	-	V
Saturated Drain Current[2] (V_{DS} = 10V, V_{GS} = 1.0V)	I_{Max}	-	20.41	-	A
Gate Threshold Voltage[3] (V_{DS} = 28V, I_{DS} = 18.5mA)	$V_{GS(TH)}$	-3.30	-2.80	-2.30	V
Gate Quiescent Voltage[3] (V_{DS} = 28V, I_{DS} = 250mA)	$V_{GS(Q)}$	-	-2.95	-	V

1. Measured on wafer prior to packaging
2. Measured with packaged device
3. Measured in Raytron DEMO

Table 6. RF Characteristics @ DEMO_A [1,2]

Freq. (GHz)	Linear Gain (dB)	Drain Efficiency (%)	Output Power (dBm)	Output Power (W)
2.7	15.2	60.6	50.1	102
2.8	15.2	62.6	49.9	98
2.9	15.3	63.3	49.6	91
3.0	15.0	63.4	49.2	83

[1]. Typical Class AB performance in Raytron DEMO_A (2.7-3.0GHz) with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 250mA, V_{GS} = -2.82V

[2]. Test Condition: CW

Table 7. Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Units
Input Capacitance	C_{GS}	-	44.7	-	pF
Output Capacitance	C_{DS}	-	8.92	-	pF
Feedback Capacitance	C_{GD}	-	1.01	-	pF

- Test Condition: V_{DS} = 28V, V_{GS} = -8V, f = 1MHz

9. Wide-band Application Performance

■ Wide-band Application Performance (DEMO_A: 2.7-3.0GHz)

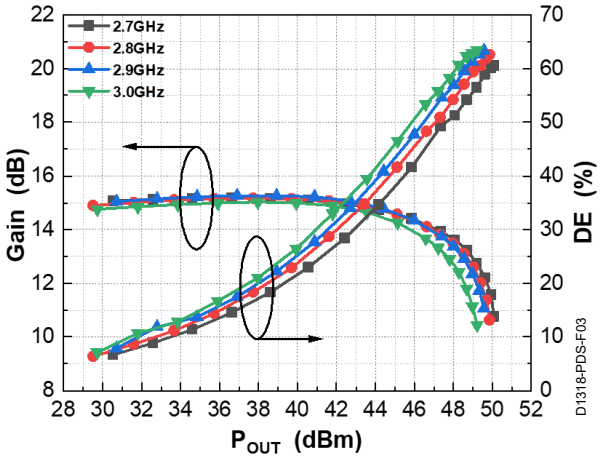


Figure 3. Wide-band Application Gain and DE vs. Output Power

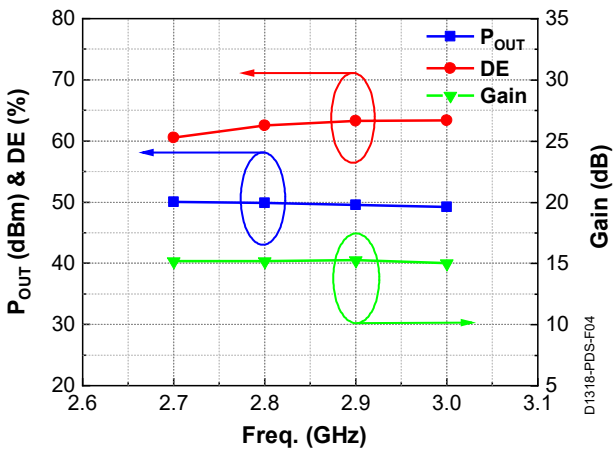


Figure 4. Wide-band Application P_{OUT}, Gain, and DE vs. Freq.

- Typical Class AB performance in Raytron Wide-band Application DEMO_A with the device soldered onto the heatsink, test condition: V_{DS} = 28V, I_{DS} = 250mA, V_{GS} = -2.82V.
- Test Condition: CW
- The output power is 49.2dBm (83W) at 2.7-3.0GHz.

10. Package Outline

■ Pin Layout

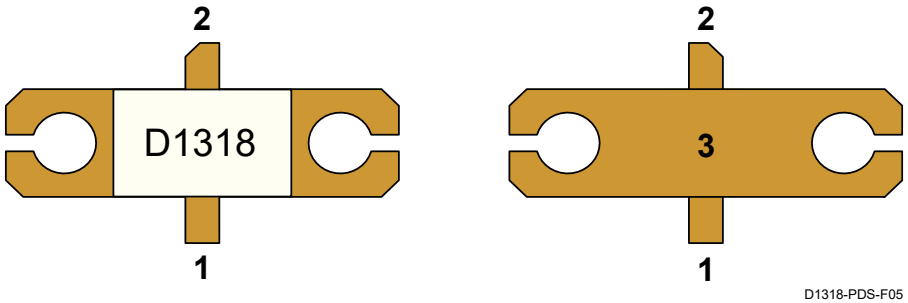


Figure 5. D1318 Pin Layout

Table 8. Pin Description

Pin	Symbol	Description
1	V_G / RF IN	Gate voltage / RF Input
2	V_D / RF OUT	Drain voltage / RF Output
3	Flange	Source to be Connected to Ground

■ Mechanical Drawing (Package Type:360F-A)

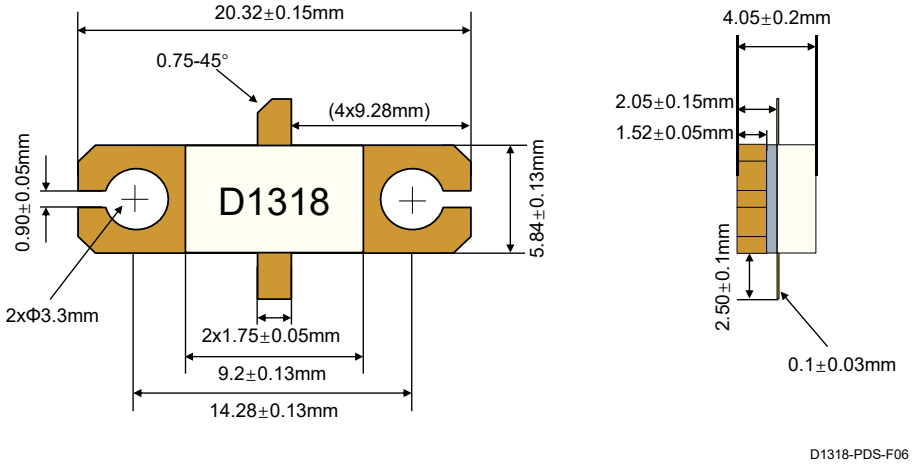


Figure 6. D1318 Mechanical Drawing

11. Application Circuit

■ Schematic

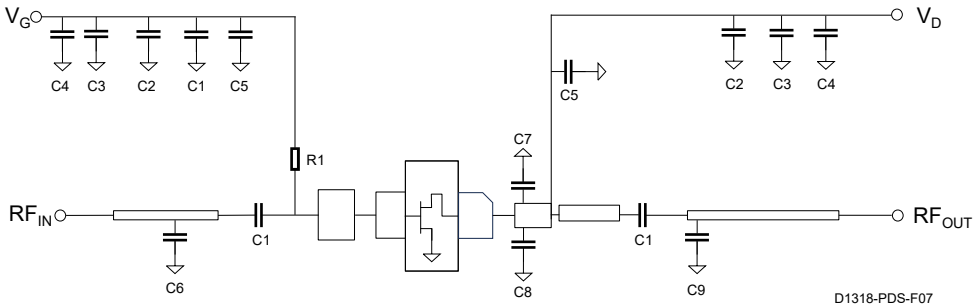


Figure 7. D1318 DEMO_A Application Circuit Schematic

■ Table 9. Bias Procedure

Bias-up Procedure	Bias-down Procedure
1) Set V_{GS} to the pinch-off voltage, typically -6V	1) Turn off RF signal
2) Turn on V_{DS} to nominal supply voltage (28V)	2) Turn off V_{DS}
3) Slowly adjust V_{GS} until I_{DS} current is attained	3) Wait two seconds to allow drain capacitor to discharge
4) Apply RF input power to desired level	4) Turn off V_{GS}

■ Layout

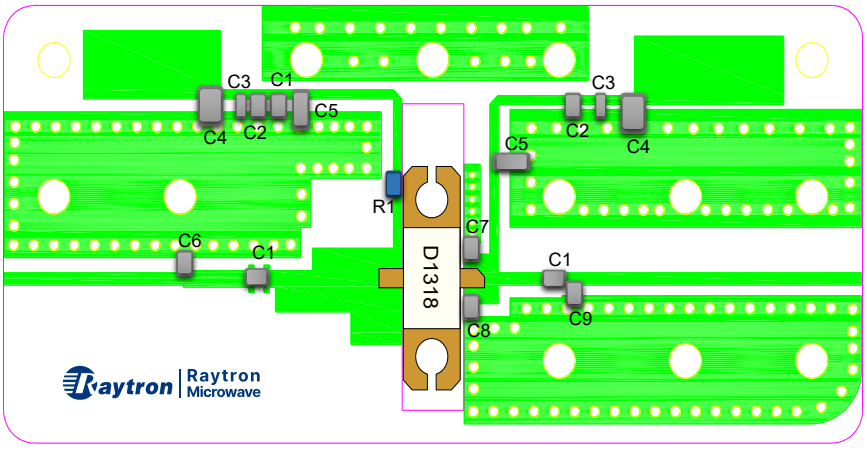


Figure 8. D1318 Application Circuit Layout
(DEMO_A Size: 8.5cm x 4.0cm)

Table 10. Bill of Materials

Component	Value	Description	Manufacturer	Part Number
PCB	20mil	N/A	Rogers	RO4350B
C1	6.0pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2E6R0CB12
C2	100pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2E101JB12
C3	1000pF	C0G 100V 0603 Capacitor	Murata	GRM1882C2A102JA01
C4	1.0uF	X7R 100V 1206 Capacitor	Murata	GRM32ER72A105KA01
C5	0.1uF	C0G 100V 1206 Capacitor	Murata	GRM3195C2A104JA01
C6	1.2pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2E1R2BB12

Component	Value	Description	Manufacturer	Part Number
C7	0.7pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2ER70WB12
C8	2.0pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2E2R0BB12
C9	0.7pF	C0G 250V 0805 Capacitor	Murata	GQM2195C2ER70WB12
R1	10Ω	1/8W 0805 Resistor	Yageo	RC0805FR-0710RL

12. DEMO Summary

Table 11. DEMO Summary

NO.	D1318 DEMO Name	V_{DS} (V)	Freq (GHz)	Linear Gain(dB)	P_{SAT} (dBm)	P_{SAT} (W)	Drain Efficiency	Pulse or CW
1	DEMO_A, 80W	28	2.7-3.0	15.0	49.2	83	60.6%	CW

13. Recommended Solder Temperature Profile

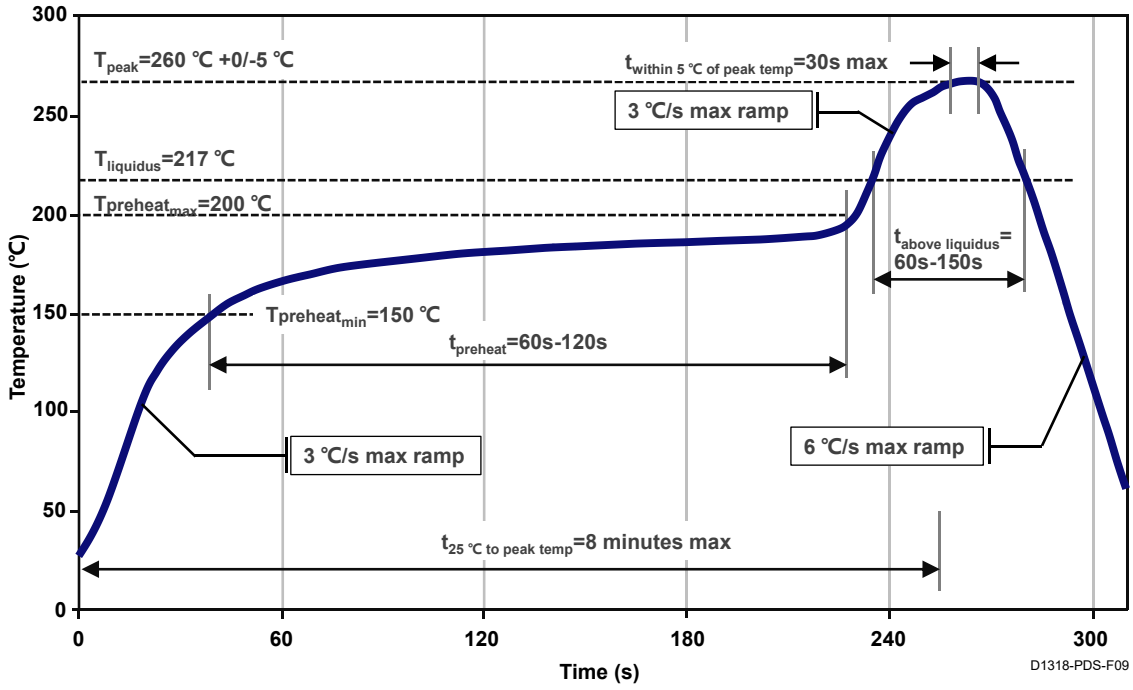


Figure 9. Recommended Solder Temperature Profile

14. Handling Precautions

■ **ESD Caution**

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore proper ESD precautions should be taken to avoid performance degradation or loss of functionality.



15. Contact Information

For the latest specifications, additional product information, technical support and sales support, please contact:

- **Email:** RF.support@raytrontek.com
- **Web:** www.raytrontek.com

16. Disclaimer

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